**RESUME OF** **K.PADMA PRIYA**

**PROFESSOR IN ECE**

 **JAWAHARLAL TECHNOLOGICAL UNIVERSITY COLLEGE OF ENGINEERING,**

**KAKINADA.**

1. Name in full : **KESARI PADMA PRIYA**

2. Date of birth : **01-06-1974**

3. Place of birth & schooling : **NELLORE (A.P)**

4. Address for Communication : **Dr. K. Padma Priya**

 **Professor**

 **ECE Department**

 **JNTUK College of Engineering**

 **Kakinada**

 **Andhrapradesh**

5. Permanent Address : **W/O K.GURUMURTHY**

 **16/218, OPPOSITE TO HEAD POST OFFICE**

 **NELLORE-524001**

 **Phone:**

 **Residence : 9247827592**

 **Cell : 9290532182**

**Email: kesaripadmapriya@jntucek.ac.in**

 **kesaripadmapriya@gmail.com**

6. Educational Qualification : **(1) Ph.D. (ECE) JNTU, HYDERABAD-**

 **June, 2009.**

**(2) M.Tech (Electronic instrumentation)**

 **NIT, Warangal.**

 **Year of passing: Nov, 2002.**

**(3) B.Tech (ECE)**

 **Nagarjuna University, Vijayawada.**

 **Year of passing: 1998.**

**(4) DECE.Govt.Polytechnic for women, Nellore.**

 **Year of passing: 1993.**

1. Field of specialization &Research Interests : **Low Power Very Large Scale Integration ( VLSI)**

8. Experience : (I) **Professor**,ECEDept,w.e.f.24-1-16 to till date, JNTUKCE,Kakinada

(II) **AssociateProfessor,**ECEDept,w.e.f.24-1-13to23-01-2016

 JNTUKCE, Kakinada

 (III) **Assistant Professor**, Electronics & Communication

 Engineering, w.e.f. 23- 11-2006 to 23-1-13, JNTUACE,

 Anantapur.

 (IV) **Assistant Professor** of Electronics & Communication

 Engineering, KSRM College of Engineering, Kadapa from

31-07-1999 to 22-11-2006.

 (V) **Teaching Assistant** in V.R. Sidhartha Engineering College, Vijayawada from 05-10-1998 to 29-07-1999.

9. Papers Published (list attached) (I) **International/National Journals** - **76**

 (II) **International/ National Conferences** - **28/23**

(III) **Symposium - 02**

1. Summer Schools & Short term

Courses attended : Attended 4 short term Courses & One Orientation course.

**Administrative Experience :**

|  |  |  |
| --- | --- | --- |
| S.No. | Administrative Post held | Duration |
|  | Head of the department, Department of ECE, UCEK, JNTU Kakinada | 14/7/2016 to 21/12/2018 (2Y.5M.8D) |
|  |  TEQIP-III, Nodal officer, Academics, UCEK,JNTUKAKINADA | 17-7- 2017 to 30-09-2021.( 4 Y- 4M-13D) |
|  | Additional Controller of Examinations (PG), JNTUK | 7/12/2019 to 6/12/21 (2years) |
|  | CO ORDINATOR APECET 2023 | 6 MONTHS (January 23 to June 2023) |
|  | Director, Green Initiatives | 29/11/2023 to |

**Achievements as Head of the Department :**

1. After 2009 during my tenure, successfully handled three different NBA committees of UG and PG inspection of the department leading to accreditation, with the support of administration and faculty,.
2. Department has extended its cooperation for NAAC inspection.
3. One of the major improvements was that I have initiated to start a Library at Department level possessing 3236 UG Text books collected from various donors/faculty members. Additionally 393 PG text books were purchased and 48 Ph.D Thesis are made available in the department library for the betterment of students and faculty.
4. Nineteen (19) guest lectures were presented by the eminent personalities from the Industry, higher learning Institutes and R&D organizations.
5. Department has successfully conducted two one week workshops and one Faculty development program
6. Department got sanctioned two new PG programmes (M.Tech-VLSI& Embedded Systems and M.Tech-Communications & Signal Processing) and class work commenced for A.Y. 2017-19.
7. Developed Advanced Communications Lab with the new equipments for the existing PG programme M.Tech C&C.
8. Developed Instrumentation Lab with the new equipments for the existing PG programme I&CE.
9. Got approval for Process Control Instrumentation lab with the new equipment for the existing PG programme M.Tech I&CE under TEQIP-III.
10. Got approval for VLSI lab for the new PG programme M.Tech VLSI & Embedded Systems under TEQIP-III.
11. Got approval from the University for faculty individual rooms and amenities.
12. Provided Faculty rooms for the Asst.Professors (contract) (earlier they were sitting in the laboratories only)
13. Separate systems with internet facility and common printer facility is also provided for the Asst.Professors (contract).(Earlier no such facilities were extended to them)
14. Separate rooms provided for the execution of funding projects.
15. Majority of the class rooms were equipped with the LCD projectors. (earlier only two class rooms were extended with such facility)
16. Developed plantation and gardening in and around the department involving student participation and coordination.
17. Recent improvements of the amenities in the department
	* Separate Toilets for Men and Women faculty members of the department,
	* Water Coolers for drinking water for students and staff for each floor.
	* Installed wash basin (for hand wash etc) facility in each floor of the department.
	* Renovation of flooring of the department in class rooms and corridors.
	* For gardening purposes water taps (with hand pipes) for watering the lawn in front of the department.
	* Lighting facility in front and in the car parking area of the department.

**Subjects Taught:**

|  |  |
| --- | --- |
| **Undergraduate courses** | **Post graduate courses** |
| 1. Electronic Devices and Circuits
2. Optical Communications
3. Digital IC applications
4. Microprocessors and Interfacing
5. Computer Organization & Architecture
6. Electronic Circuit Analysis
7. VLSI Technology and Design
8. Structured Digital Design through VERILOG
9. Linear IC’s and its applications
10. Structural Digital Design Through VERILOG
11. Digital IC Design
 | 1. Optical communication & networks
2. CPLD & FPGA Architectures and Algorithms
3. Soft computing techniques
4. Low Power VLSI Design
5. VLSI Technology and Design
6. SoC (System on Chip)
7. Fiber optic Sensors & Actuators.
8. Analog & Digital IC Design
 |

In addition, the laboratories of UG and PG programmes in the areas of Electronics and Computers have been dealt with.

**Research Experience and Guidance provided:**

Total research experience : 13 years

* Guided 50+ Undergraduate projects and 30 Postgraduate thesises.
* Guided students to work and present the research papers in student paper contests
* Presently, 7 supervising Research scholars for their Ph. D

**Details of Ph. Ds’ awarded in my guidance:**

|  |  |  |  |
| --- | --- | --- | --- |
| S.NO. | Name of the Research Scholar | Title of the Thesis | Year of award |
| 1 | G.Srinivasulu(10PH0413) | Design of low power high speed level shifters for multi vdd systems | 2017, JNTUATP |
| 2 | Dodda Basavanna gouda (11PH0420) | A Novel Instrumentation System for Seperation and Detection of Whole Blood components | JUNE 2018 JNTUATP |
| 3 | R.Kalyan (11PH0430) | Ultra wide band compact band notch micro strip antennas for wireless telecommunication applications | Dec, 2020.JNTU ATP  |
| 4 | V Madhurima Roll no: 14022P0428 | Low power Hardware Accelerator Architecture for Intraprediction in Video Coding | April 2023JNTUKAKINADA |
| 5 |  |  |  |

**Details of Ph. D theses submitted for evaluation:**

|  |  |  |  |
| --- | --- | --- | --- |
| Name of the Research Scholar | Title of the Thesis | Year of submission | Name of the University |
| Mrs. M.Manikumari (13022P0431) | Reduction of Delay and Power in VLSI Interconnects using Multiple Valued Logic | July 2023 | JNTUKakinada |

**List of M.Tech Projects Guided**

|  |  |  |  |
| --- | --- | --- | --- |
| S.No. | Name of the student | Roll No. | Title of the Project |
|  | K.Gurumurthy | D1082-MTE-26866(M.Tech-IT) | Efficient on-chip Crosstalk Avoidance CODEC design in VLSI circuits. |
|  | K.Anilkumar | 08001D3811(DECS) | Resource Allocation in an efficient manner for the wire less cellular systems. |
|  | K.Ravikumar | 05004D0616(DSCE)(SSS) | Selected Mapping Algorithm for PAPR reduction of Space-Frequency coded OFDM System without side Information. |
|  | G.Jyothi | 10001D4118(DSCE) | Data Security using Secure Hash Algorithm -2. |
|  | CH.Sunilkumar | 10001D4124(DSCE) | Implementation of Concurrent online test in RFID Memories. |
|  | S.Divya | 10001D4206(DECS) | Configurable Space Allocation with Built in Redundancy Analysis. |
|  | V.Shavali | 08001D3804(DECS) | Data bus encoding technique to reduce power in VLSI circuits. |
|  | P.MuraliKrishna | 09004D0606(DSCE) (SSS) | QDGFET Based Design of Ternary Logic Combinational Circuits. |
|  | Shaik JohnySaidha | 13021D1416(I&CS) | Control of Calorie Intake Based on Body Mass Index |
|  | K.Sriramya | 13021D3604(C&C) | MultiBranch Tomilson-Harashima Precoding for MU-MIMO System with Quantized CSI |
|  | K.Shaloam Suvarna Sujan | 13026D3714 | Development of VLSI based Environment for the feature extraction of an ECG signal using ADSP Processor |
|  | K.NagaSurendra | 13026D3715 | FPGA Implementation of Contrast Enhancement Algorithm |
|  | P.Balakiran | 14021D410  | Implementation of Power Efficient 12-bit Vedic Multiplier by using ANT Architecture |
|  | PAK.Kishor | 14021D3620 | Low Power Clock Distribution Network Using Clock Paired Shared FlipFlop |
|  | P.Srikiranmai | 14021D3617 | Implementation of Area Efficient 12-Bit DADDA Multiplier y using ANT Architecture |
|  | A.Tharun Kumar | 14026D3710(VLSI- SEER Academy)) | A Low POWER dynamic TCAM Using Master Slave Match Line Architecture in HSPICE |
|  | G.Sirishkumar  | 14026D3723(VLSI- SEER Academy)) | Design of Low Power MAC using modified Booth Recorder |
|  | G.AtulyaRatna | 14026D3704 (VLSI- SEER Academy)) | A Post Rourting Stage IR Drop Reduction Technique with less Routing Resources |
|  | T.MahalakshmiMounika | 15021D3619 | A New Sensor Based Communications for Environmental Monitoring |
|  | K.Sridevi | 15021D3613 | Development and Performance Evaluation of IRIS recognition in the presence of occluded region. |
|  | T.Gangaraju | 16021D3622(C &C) | Low Dense and Efficient Convolute Blind source separation (CBSS) design using modified booth encoding algorithm |
|  | Challa.Naga sai Ravali | 17021D8022 (C & SP) | Design and implementation of blue tooth-Beacon based indoor positioning system. |
|  | K.Harika | 17021D3616 (C &C) | Cluster based secure revocation authentication scheme for vehicular ad-hoc networks |
|  | Lanka Martha Esther | 17021D6819(VLSI & Embedded system) | Implementation of Fast Fourier Transform (FFT) on FPGA using Verilog HDL. |
|  | Y. Yamini priya | 17021D6804 (VLSI & Embedded system) | Water Quality Monitoring System using Internet of Things (IoT) |
|  | Pilli vinay | 18021D1407(Instrumentation and Control Engineering) | A fog enabled health care framework for corona virus |
|  | M.Bhaskara Satyanarayana | 18021D8011(C&SP) | Generation and validation of software defined radio based spoofed GPS signals for securing locations. |
|  | Yenti Lakshmi Teja Sree | 19021D3621(C&C) | An Efficient and Secured trust based Intrusion Detection System |
|  | Nukapeyyi Tanuja | 19021D8016(c&sp) | Medical image fusion using deep learning mechanism |
|  | Vinukollu Lakshman | 19021D6804(VLSI&ES) | Area efficient carry look ahead adder based on memristor ratioed logic |
|  | Emani Pravallika | 20021D6816(VLSI & ES) | Design of Multiplier using Dual stage Compressor with reversable logic |
|  | Anantatmakula Subhadra | 20021D3622(C&C) | Robust features for identification of copy-move forgery |
|  | Chilakalapudi Naga Venkata Tirumala | 20021D3606(C&C) | Sand dust image enhancement using histogram and L-CLAHE Technique |
|  | Podili Jyothi Surekha | 20021D8016(C&SP) | Detection of Proliferative Diabetic Retinopathy using Histogram of Oriented Gradients |
|  | Gedela Kavitha | 20021D1408 ( I&CE) | Speed Control of CUK Converter fed DC Motor using Fractional Order PID Controller |
|  | CH.Kavya | 21021D6803(VLSI & ES) | Ml-based ionospheric scintillationsClassification and real-time display usingRaspberry pi |
|  | A.RamaDevi | 21021D6802(VLSI & ES) | WI-FI and Raspberry pi based system for service area estimation of drone mounted source. |

**Other Experiences:**

**UPSC ADVISOR/EXPERT: (i) Attended during 15-17 of December, 2021, at UPSC**

 **bhavan, New Delhi.**

 **(ii) Attended during 18-22 of April, 2022, at UPSC**

 **bhavan, New Delhi.**

**(iii)** **) Attended during 17-18 of July, 2023, at UPSC**

 **bhavan, New Delhi.**

**ECET -2023 coordinator**

**Ph.D thesis evaluator/Examiner:**

* AndhraUniversity, Visakhapatnam

Vignan University,Guntur

JNTU Anantapur

JNT University, Hyderabad

S.V.University

KLU, Vijayawada

ANU,Guntur

**Curriculum Design:**

* Member of Board of studies For JNTU Kakinada from the Year 2016.
* Chairman of Board of studies For ECE dept, UCEK (A), JNTU Kakinada from the Year 2016-18.

**List of awards:**

* **Best Teacher Award for the year 2016-17, from ECED, UCEK, JNTUK, Kakinada.**

**List of Universities / Deemed Universities in which I am serving as examiner/paper setter/evaluator:**

JNTUK, Kakinada

JNTU Hyderabad

JNTU Pulivendula

JNTU Anantapur

AndhraUniversity

Vignan University, Guntur

SITAMS Chittore

KLU,VIJAYAWADA

Vasavi College of Engineering (A), Hyderabad

QIS College of Engineering (A), Ongole.

Srividyanikethan Engineering College,

 Tirupati, JNTUA

**Important positions held:**

* **Head of ECE department**, ECE Department, UCEK,

 JNTUKakinada from 14/7/2016 to 21/12/2018 (2Y.5M.8D)

Appointed as **TEQIP-III, Nodal officer, Academics**, from 17-7- 2017 to 30-09-2021.( 4 Y- 4M-13D)

**Appointed as Additional Controller (PG**) from 7/12/2019 to 6/12/21.

Appointed as Director green Initiatives from 29 November 2023.

Member of the Governing council of Affiliated Institutions to JNTUKakinada as a University nominee.

University nominee for BOS for the affiliated colleges (SRKR bhimavaram & Raghu Engineering College, Visakhapatnam).

Convener for Fact Finding Committee to Affiliated Institutions to JNTUKakinada.

Member for Fact Finding Committee to Affiliated Institutions to JNTUKakinada. Member of faculty ratifications in JNTUKakinada.

External M.Tech Project Viva-voce Examiner to Affiliated Institutions to JNTUKakinada.

Member of Malpracticing committees at JNTUKakinada.

Member for challenged evaluations for different courses in JNTUKakinada.

Ph.D thesis evaluator & viva-voce examiner for AndhraUniversity Visakhapatnam, Osmania University, Hyderabad,SV University, Tirupathi,ANU,Guntur..

APECET 2023, CO ORDINATOR

**Social Service Activities**

* Actively involved in Janma Bhoomi programmes of Government of Andhra Pradesh in JNTUKakinada

**(Dr.K.PADMAPRIYA)**

**LIST OF JOURNAL PUBLICATIONS**

1. M. Mani Kumari, Dr.K.Padma priya,,” MULTIBIT MEMORY CELL DESIGN USING MULTIPLE VALUED LOGIC” International Journal of Engineering Applied Sciences and Technology, 2022 Vol. 8, Issue 01, ISSN No. 2455-2143, Pages 73-77 Published Online May 2023 in IJEAST,pp 73-77.IJEAST-Unpaid Journal.
2. Dr.V.Radhika, Dr.K.Padma priya, B.Vijaya Lakshmi,K.Bhageerath,”Optimization of Spectrum Sensing for cognitive radio using Genetic algorithms”,International summit on Quality Indices in Higher Education, Dated 23-25, March 2023, ESCI Hyderabad.
3. Dr.V.Radhika, Dr.K.Padma priya, B.Vijaya Lakshmi,” Bio Medical application with Variational level set classifier tool”, December 2022 | IJIRT | Volume 9 Issue 7 | ISSN: 2349-6002,pp.39-44. **(UGC CARE)**
4. Chettukrindi Mohammad Aslam , Donti Satya Narayana , Kesari Padma Priya,” Detection of Breast Cancer Using Modified Markov Model” ,IIETA, Traitement du Signal Vol. 39, No. 6, December, 2022, pp. 2195-2201.
5. K. Yuva Vidyasagar, K. Padma Priya,” Obtaining Stability Regions using Random Sampling in Time Delay Handling Method in Dominant Pole Placement based PID Controller Design”, International Journal of Innovative Research in Science, Engineering and Technology (IJIRSET) , e-ISSN: 2319-8753, p-ISSN: 2347-6710,pp. 13390-13399. **Nov 22**
6. Emani Pravallika,.K.Padma priya” Design of Multiplier using Dual stage Compressor with reversable logic”, JETIR October 2022, Volume 9, Issue10,pp.d250-257, **UGC Approved Journal**. No 63975,ISSN: 2349-5162
7. A. Subhadra, K. Padma Priya” Robust features for identification of copy-move forgery” JETIR October 2022, Volume 9, Issue10,pp.d250-257, UGC Approved Journal No 63975,ISSN: 2349-5162
8. Ch.N.V.Tirumala,K.Padma Priya,” Sand dust image enhancement using histogram and L-CLAHE Technique “JETIR October 2022, Volume 9, Issue10,pp.d250-257, UGC Approved Journal No 63975,ISSN: 2349-5162
9. Podili Jyothi Surekha,K.Padma Priya,”Detection of Proliferative Diabetic Retinopathy using Histogram of Oriented Gradients” “JETIR October 2022, Volume 9, Issue10,pp.d250-257, UGC Approved Journal No 63975,ISSN: 2349-5162
10. Dr.V.Radhika, Dr.K.Padma priya, Dr. A.Srinivas,” BIO MEDICAL APPLICATIONS USING OPTIMUM STATISTICAL CLASSIFIER TOOL”, AEGAEUM JOURNAL Volume 10, Issue 7, 2022 ISSN: 0776-3808,pp 20-28. (UGC CARE)
11. V.Madhurima, K.PadmaPriya,” HEVC Encoding and Decoding using Fast Algorithm for Intra Frame Prediction Using DTCWT” Microelectronics and Telecommunication Engineering, Lecture notes in network and systems series (LNNS, Volume 373) ISBN: 978-981-16-8720-4 | DOI:10.1007/978-981-16-8721-1\_69 | Pages: 759-773.
12. V.Madhurima, K.PadmaPriya” ASIC Implementation of Hardware Efficient DTCWT Architecture for Intra Prediction HEVC Coding in Complex Wavelet” Ingénierie des Systèmes d’Information Vol. 27, No. 2, April, 2022, pp. 193-204. **SSN:**1633-1311 (print); 2116-7125 (online) (SCOPUS)
13. V.Madhurima, K.PadmaPriya “FPGA Implementation of High Speed distributed arithmetic optimum memory utilized DTCWT architecture for image processing.” Design engineering,issue:6,pp.331-1346,ISSN:0011-9342,April 2021) (SCOPUS)
14. Lanka Martha Esther, Mr. Gaurav Gupta, Dr.K.Padma priya,” IMPLEMENTATION OF FAST FOURIER TRANSFORM (FFT) ON FPGA USING VERILOG HDL” JETIR June 2019, Volume 6, Issue 6, jetir.org (ISSN-2349-5162),pp716-719. (UGC APPROVED NO. 63975)
15. Y.Yaminipriya, K.PadmaPriya, Measuring of Water Quality Parameters Using Internet of Things (IOT) ” has been Accepted, Registered and Published (IJETA-V6I4P4) , in July-Aug 2019, Volume No. 6 Issue 4 with ISSN: 2393-9616 & ISO 3297:2007.
16. K.Harika, Dr.K.Padma Priya” Cluster Based Secure Revocation Authentication Scheme for Vehicular Ad-Hoc Networks” International Journal of Engineering Trends and Applications (IJETA) – Volume 6 Issue 4, Jul-Aug 2019, ISSN: 2393-9516,pp. 15-18.
17. M. Mani Kumari, K. Padma Priya,” SRAM based Look Up Table Design Using Multiple Valued Logic”, Jour of Adv Research in Dynamical & Control Systems, Vol. 11, 07-Special Issue, 2019, ISSN 1943-023X ,pp-157-160. Scopus SJR (Scopus coverage: from 2009 to 2015, from 2017 to August 2020)
18. W. Yasmeen, G.N. Swamy, K. Padma Priya, “Design of Ultra Wide Band Low Noise Amplifier for 3-5 GHz Using 0.18 µm” Jour of Adv Research in Dynamical & Control Systems, Vol. 11, 07-Special Issue, 2019, ISSN 1943-023X,pp:105-110. Scopus SJR (Scopus coverage: from 2009 to 2015, from 2017 to August 2020)
19. V.Madhurima, K.PadmaPriya” FIR Filter Design Using Modified DAA for Low Power Applications” Jour of Adv Research in Dynamical & Control Systems, Vol. 11, 07-Special Issue, 2019, ISSN 1943-023X 111 Received: 17 June 2019/Accepted: 30 June 2019,pp:111-118. Scopus SJR (Scopus coverage: from 2009 to 2015, from 2017 to August 2020)
20. A.Mallaiah , G.N.Swamy K.PadmaPriya” A Novel Design of Majority Gate (Three Variable) Circuits and Comparative Analysis using Quantum-Dot Cellular Automata (QCA)”, Jour of Adv Research in Dynamical & Control Systems, Vol. 11, 07-Special Issue, ISSN 1943-023X 10 Received: 15 June 2019/Accepted: 30 June 2019 2019,PP.1-10 SJR (Scopus coverage: from 2009 to 2015, from 2017 to August 2020)
21. A.Mallaiah , G.N.Swamy K.PadmaPriya”Nanocell Parity Generator (PG) Circuits Design and Power Dissipation Analysis” Jour of Adv Research in Dynamical & Control Systems, Vol. 11, 07-Special Issue, 2019, ISSN 1943-023X Received: 18 June 2019/Accepted: 30 June 2019,PP-139-144. SJR (Scopus coverage: from 2009 to 2015, from 2017 to August 2020)
22. R.Kalyan , K.T.V.Reddy , K.Padma Priya” Compact CSRR Etched UWB Microstrip Antenna with Quadruple Band Refusal Characteristics for Short Distance Wireless Communication Applications” Progress In Electromagnetics Research Letters, Vol. 82, 139–146, 2019.
23. T.Gangaraju, K.Padma Priya,” Low Dense and Efficient Convolute Blind source separation (CBSS) design using modified booth encoding algorithm”JETIR,,October 2018,Volume 5, Issue 10.pp.199-205. ([**UGC Approved Journal no 63975**](https://jetir.org/jetir%20ugc%20approval.pdf)**, ISSN: 2349-5162)**
24. A.Mallaiah , G.N.Swamy K.Padma Priya”Designing Efficient Multiplexer, Demultiplexer QCA logic circuits and Power Dissipation Analysis –A new approach International Journal of Scientific & Engineering Research , Volume 9, Issue 7, July-2018 ISSN 2229-5518,pp. 1995 to 2004.
25. Mohammad Aslam.c, satya Narayana.D, Padma Priya.k “A novel approach for image segmentation” International Journal of Research and Analytical Reviews, Volume 5,Issue 4,DEC.2018.
26. R.Kalyan , K.T.V.Reddy , K.Padma Priya” Novel UWB microstrip antenna with dual band-notched characteristics for short distance wireless applications” Journal of Engineering Technology, Volume 6, Issue 1, Jan. 2018, PP.584-594.
27. A.Mallaiah , G.N.Swamy K.PadmaPriya”1bit and 2bit comparator designs and analysis for quantum dot cellular automata, nanosystems: physics, chemistry, mathematics, 2017, 8 (6), p. 709–716.
28. Mohammad Aslam.C, Satya Narayana.D, Padma Priya.k, “Segmentation of Rumex obtrusifolius using simple fuzzy logic and Gmrf ” International Journal of Engineering Research and Application Volume7 Issue11 Nov.2017.
29. Mohammad Aslam.C, Satya Narayana.D, Padma Priya.k, “Tumor detection using Trapezoidal Rayleigh distribution” IOSR Journal of Electronics and Communication Engineering Volume12, Issue 4,Jul-Aug 20117.
30. T.M.L.Mounica,K.PadmaPriya” A New Sensor Based Communications for Environmental Monitoring “International Journal of Future Revolution in Computer Science & Communication Engineering”, pp.168-171,ISSN:2454-4248.
31. K.Sridevi,K.Padmapriya,”Development and Performance Evaluation of IRIS recognition in the presence of occluded region”,International Journal of Research, Vol.04, Issue 13, pp.456-460.e-ISSN:2348-6848,p-ISSN: 2348-795x.
32. R.Kalyan,Dr. K.T.V.Redd2, Dr. K.Padma Priya” compact band notch ultra wideband microstrip antenna for space ship applications,International Journal of Recent Trends in VLSI, Embedded Systems and Signal Processing,Volume-2-Issue-5-2017,ISSN-2454-2407,pp30-32.
33. A.Mallaih,G.N.Swamy,K.PadmaPriya”Effect of Orbital on *I*/*V* Characteristics and Transmission in Molecular Diode Structures with Au Contacts” *JOURNAL OF NANO- AND ELECTRONIC PHYSICS,* Vol. **9** No 1, 01001(5pp) (2017).
34. G.Sirishkumar,K.PadmaPriya,” Design of Low Power MAC using modified Booth Recorder” International Journal of Innovative Research in Computer and Communication Engineering,pp.19769-19774.ISSN(online):2320-9801:ISSN(print)2320-9798.
35. A.Tharun Kumar, K.PadmaPriya,” A Low POWER dynamic TCAM Using Master Slave Match Line Architecture in HSPICE”, International Journal of Innovative Research in Science, Engineering and Technology”pp.20304-20312. ISSN(online):2319-8753:ISSN(print)2347-6710.
36. Kishore,k.Padmapriya,” low power clock distribution network Using clock paired shared flip flop”Vol. 5, No. 4, October 2016,ISSN 2319 – 2518.
37. BalaKiran,k.Padmapriya,” Implementation of power efficient 12-bit vedic multiplier by using ant architecture”Vol. 5, No. 4, October 2016,ISSN 2319 – 2518.
38. Kiranmai, k.Padmapriya” Implementation of area efficient 12-bit dadda multiplier by using ant architecture Vol. 5, No. 4, October 2016,ISSN 2319 – 2518.
39. A.Mallaih,G.N.Swamy,K.PadmaPriya”Assymetric Molecular Diode energy calculation using extended Huckel and Parametric method” Nano systems physics, chemistry mathematics, 2016, Vol7(3),pp 569-574.
40. A.Mallaih,G.N.Swamy,K.PadmaPriya” Observation of Terminal Atom Effect on Charge in NH2- Cn-NO2 Molecule: A Hartree- Fock Theory”, Communications on Applied Electronics (CAE) – ISSN : 2394-4714 Foundation of Computer Science FCS, New York, USA Volume 5– No. 2, May 2016.
41. K.Padmapriya,Aparna et.al.,” “Delay optimization of 32-bit carry skip adder” International Journal of Electrical and Electronics Engineering and Telecommunications, Vol. 5, No. 2, April 2016,pp.27-32.
42. W.Yasmeen,S.NagarajaRao,K.Padmapriya,”The design and optimization of UWB PHY Transreceiver IC by using CMOS Technology” *International Journal of System Design and Communication Systems,Vol3,Issue 10,December 2015,pp1442-1445.(If.3.014)*
43. Srinivas, Ramanaiah, K.padmapriya” Area and energy efficient Intelligent level shifter”, Research journal of Applied Sciences, Engineering and Technology, June,15, 201510(5).ISSN:2040-7459,e-ISSN 2040-7467, pp. 532-536.
44. Doddabasavana Goud.B, K.Padma Priya,”a comprehensive review on estimating the blood cell count using various Advanced techniques”, *International Journal of Latest Research in Science and Technology,* **ISSN (Online):2278-5299***Volume 4, Issue 2: Page No.103-105, March-April 2015.*ISSN:2278-5299 103 (impact factor 2.51)
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**PAPERS PRESENTED IN NATIONAL CONFERENCES**

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4. M. Mani Kumari, K.PadmaPriya” Design of Latched Comparator Using Multiple Valued Logic” a Two day National Level Conference “VSPICE 2K18” during 19-20, July at ECE Department, JNTUK UCE, Kakinada, ISBN:978-93-85100-99-4.
5. Doddabasavana Goud.B, K.Padma Priya”A Simple Portable Red Blood Cell Counter using Centrifugal Technique”NERIET-14,ISBN:978-1-63041-810-6,pp.1-4.
6. R.kalyan, Dr.K.T.V Reddy, Dr.K.PadmaPriya “compact band notch ultra wideband microstrip antenna for spaceship applications” National conference on Advancements in remote Sensing, ISSSN:2349-8439.
7. Doddabasavana Goud.B, K.Padma Priya” A General Review of Practical Centrifuges Used to Detect RBC & WBC Count Values” a Two Day National Level Conference “VSPICE 2K15” during 6-7, November, 2015” at ECE Department, JNTUK UCE, Kakinada, ,pp23-28.ISBN:978-93-5235-967-7.
8. A.Mallaih,G.N.Swamy,K.PadmaPriya”Observation of terminal atom effect on charge in NH2-Cn-NO2 molecule-A Hortree –Fock –Theory”, a Two Day National Level Conference “VSPICE 2K15” during 6-7, November, 2015” at ECE Department, JNTUK UCE, Kakinada, ,pp14-16.ISBN:978-93-5235-967-7.
9. K.Sri Ramya, K.Padma Priya, “Multi-Branch Tomlinson-harashima Predecoding for MU-MIMO system with quatized CSI” ”, a Two Day National Level Conference “VSPICE 2K15” during 6-7, November, 2015” at ECE Department, JNTUK UCE, Kakinada, ,ISBN:978-93-5235-967-7
10. R.kalyam, K.T.V.Reddy, K.Padma Priya, ”Single feed multi frequency ultra wideband Rectangular patch Micro strip Antenna for wireless telecommunication applications” ”, a Two Day National Level Conference “VSPICE 2K15” during 6-7, November, 2015” at ECE Department, JNTUK UCE, Kakinada, ISBN:978-93-5235-967-7.

1. M.Mani Kumari, K,PadmaPriya “ Boostable Repeater design with dual level adaptive supply voltage for variation resiliene interconnects”, a Two Day National Level Conference “VSPICE 2K15” during 6-7, November, 2015” at ECE Department, JNTUK UCE, Kakinada, ,pp33-36.ISBN:978-93-5235-967-7.
2. Srinivas, Ramanaiah, K.padmapriya” Low Power Energy Efficient CMOS Voltage Level Shifter for Multi VDD Systems” a Two Day National Level Conference “VSPICE 2K15” during 6-7, November, 2015” at ECE Department, JNTUK UCE, Kakinada, ,pp53-56.ISBN:978-93-5235-967-7.
3. K.Padma Priya” Analysis of Wire Redundancy in Bus Encoding Techniques for Deep Sub Micron Technology-A Review”, a Two Day National Level Conference “VSPICE 2K15” during 6-7, November, 2015” at ECE Department, JNTUK UCE, Kakinada, ,pp77-78. ISBN:978-93-5235-967-7.

1. Madhurima, K.padmapriya”A Review- Ground bounce noise minimization Techniques to increase the performance in multi threshold CMOS circuits”, a Two Day National Level Conference “VSPICE 2K15” during 6-7, November, 2015” at ECE Department, JNTUK UCE, Kakinada, ,pp62-66. ISBN:978-93-5235-967-7.
2. P.Raja, K.padmapriya,” A survey of optimization Techniques in High Level Synthesis”, a Two Day National Level Conference “VSPICE 2K15” during 6-7, November, 2015” at ECE Department, JNTUK UCE, Kakinada, ,pp72-76. ISBN:978-93-5235-967-7
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5. Jhony saida, K.Padmapriya,”Control of calarie in take based on BMI”, a Two Day National Level Conference “VSPICE 2K15” during 6-7, November, 2015” at ECE Department, JNTUK UCE, Kakinada, ,pp221-225. ISBN:978-93-5235-967
6. K.Sriramya,K.Padmapriya,”Muitibranch –Tomilson Precoding for MU-MIMO system with quantized CSI”, a Two Day National Level Conference “VSPICE 2K15” during 6-7, November, 2015” at ECE Department, JNTUK UCE, Kakinada, ,pp281-285. ISBN:978-93-5235-967-7.
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**PAPERS PRESENTED IN INTERNATIONAL CONFERENCES**

1. G,Kavitha,K.Padmapriya,”Speed control of CUK converterFED DE motor using fractional order PID controller” International Conference on Communication and Signal Processing, (ICCSP 2022) organized by the Department of Electronics and Communication Engineering on 27th & 28th September 2022, QIS COLLEGE OF ENGINEERING AND TECHNOLOGY,Ongole, A.P.
2. R.Kalyan, Dr. K.T.V.Reddy, Dr. K.Padma Priya”A Novel Ultra-Wide Band (UWB) Antenna with Dual Band Notch Characteristics for Short Distance Wireless Telecommunication Applications” Joint International Conference on Artificial Intelligence and Evolutionary Computations in Engineering Systems (ICAIECES-2017) & Power, Circuit and Information Technologies (ICPCIT-2017)
3. R.Kalyan, Dr. K.T.V.Reddy, Dr. K.Padma Priya”Compact Band Notch Ultra Wide Band Microstrip Antenna for Short distance wireless Applications” International Conference on Inventive Communication and Computational Technologies,(ICICCT 2017) 978-1-5090-5297-4/17/$31.00 ©2017 IEEE pp.66-69.
4. G.AtulyaRatna,K.PadmaPriya,” A Post Rourting Stage IR Drop Reduction Technique with less Routing Resources”2016, IEEE International Conferenceon Computational Intelligence and Computing Research”,Dec15-17,Agni College of Technology ,Chennai pp. 616-621.

## Doddabasavana Goud.B, K.Padma Priya,”[FPGA based instrumentation for seperation and evaluation of RBC and WBC count values using empirical law](http://ieeexplore.ieee.org/xpl/articleDetails.jsp?arnumber=7443123&filter%3DAND%28p_IS_Number%3A7443105%29)” 2015 Annual IEEE India Conference (INDICON), Date 17-20 Dec. 2015, 978-1-4673-6540-6/15.

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2. K. Shaloam Suvarna Sujan, R. Sai Pridhvi, R. Venkata Ramana, Dr. K. Padma Priya “Preformance analysis for the feature extraction algorithm of an ECG signal” IEEE sponsored 2nd International conference on Innovations in Information Embedded and Communication systems ICIIECS 2015, Department of Electronics and communications Engineering, Karpagam College of Engineering,Coimbatore,19’th-20’th,mar,2015, ISBN:978-7-4799-6816-9.
3. Doddabasavana Goud.B, K.Padma Priya” FPGA based Instrumentation for Separation and evaluation of RBC&WBC Count using Empirical law” 12’th India International conference INDICON 2015,Department of electrical Engineering, Faculty of Engineering and Technology, Jamia Millia Islamia, NewDelhi,17’th -20’th,Dec, 2015,ISBN:978-1-4673-6540-6/15.
4. Srinivas, Ramanaiah, K.padmapriya,” High Speed Energy Efficient Level Shifter for multi Core Processors”, lnternational Conference on Circuits, Communication, Control and Computing, I4C 2014, 21-22 November 2O14, M S Ramaiah Institute of Technology,Banglore,pp.393-397.
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7. Srinivasulu Gundala, Venkata K. Ramanaiah, PadmaPriya kesari” Nano second delay level shifter with logic level correction ” International conference on Advances in Electronics, Computers and Communications(IEEE-2014)
8. Doddabasavana Goud.B, K.Padma Priya,”Optimization of capillary tube and speed control of dc motor for centrifuge technique”,II International Conference on Current Trends in Engineering and Management(ICCTEM 2014),17-19 july, 2014, Vidyavardhake College of Engineering, Mysore, Karnataka.
9. P.Ramesh,Ushasree,K.Padmapriya,”A Novel Hybrid Inaudible Audio Watermarking with Binary Image as Watermarking using DWT and SVD” International conference on “ SMART SYSTEMS”(ICSS-2013) on 7-8, October 2013, JBIT,Hyderabad,pp.97-101.
10. Mohammad Aslam.c, satya Narayana.D, Padma Priya.k ”classification and analysis of morphological edge detectors ” International Conference on”SMART SYSTEMS”(ICSS-2013) on 7-8, October 2013,Hyderabad.
11. Geeta,Manjunathachari,K.Padmapriya,Sangeeta,”Implement Descrete wavelet Transform and principal component analysis to digital video watermarking” A two days National Conference on Advanced Communication Systems and Applications”25-26 of june 2013,Madanapalli Institute of Technology and Science, Madanapaleey,pp.88-91.
12. K.Padma Priya “low power bus coding technique for minimizing capacitive crosstalk in DSM technology” International Journal of Engineering Research and Science & Technology, volume 2,Issue 2,May 2013.
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14. P.MuraliKrishna,G.Jyosthna, K.padmapriya,”The Wireless Sensor Network for Homecare”, International conference on Computer Science and Engineering-ICCSE2012,IRD,Bhuvaneswar.
15. Y.Mahesh,S.Varadarajan,K.padmapriya,”Design of optimal wavelets for infrared image enhancement and dim target detection”, 4’th International conference on Science Engineering and Technology, organised by school of Information Technology and Engineering,May 3-4,2012, VIT, Vellore.
16. Y.Mahesh,S.Varadarajan,K.padmapriya,”Infrared Image Enhancement and Target detection using wavelets””, International conference on innovation in Electronics and Communication Engineering(ICIECE-2012), 20-21,July,2012, GNI, Hyderabad.
17. Y.Mahesh,S.Varadarajan,K.padmapriya,”Design of wavelets for Image enhancement and target detection”, International conference on innovation in Electronics and Communication Engineering(ICIECE-2012), 20-21, July,2012, GNI, Hyderabad.
18. Y.Mahesh,S.Varadarajan,K.padmapriya,”Infrared Image Enhancement Based on Wavelet Transformation and Retinex”, 4,th International conference on Science Engineering and Technology, organised by school of Information Technology and Engineering,May 3-4,2012, VIT, Vellore.
19. G.Jyothi, K.Padmapriya,”Secret Information Authentication”, International Conference on Computating and Communication, Technology (ICCCTN-12), May 30-31, 2012, held at Immanuel Arsar, JJ College of Engineering, Nattalam, Kanyakumari,Tamilnadu.
20. S.Divya, K.Padmapriya,”Configurable Spare Allocation with Built in Redundancy Analysis” International Conference on Computating and Communication, Technology (ICCCTN-12), May 30-31, 2012, held at Immanuel Arsar JJ College of Engineering, Nattalam, Kanyakumari,Tamilnadu.
21. Doddabasavana Goud.B, K.Padma Priya, Nagabhushana Katte, ”A Review of Recent Advances in Separation and Detection of Whole Blood Components” published International Conference on Communication, Computation, Management and Nanotechnology (ICN-2011), September, 23-25, 2011, held at Rural Engineering College, Bhalki, Karnataka, India.(sponsored by VTU, Belgaum, ISTE, IETE & MGSSK ,Bhalki).
22. K.Padma priya,CH.D.V.Paradesi Rao, “Reduction of Switching Activity for Low Power VLSI” published in the International Conference on Signal Processing, Communications and Networking (ICSCN 2008),Anna University, Chennai (sponsored by IEEE),Jan 4-6,2008, pp.566-567.
23. K.Padma priya,CH.D.V.Paradesi Rao, “An Exclusion Algorithm for Reduction of Bus Transitions in Low power VLSI”, International conference on Information and Communication Technology in Electrical Sciences, organized by IET (formerly IEE),Dec 20-22,2007.pp.822-825.
24. K.Padma priya,CH.D.V.Paradesi Rao, “Dynamic Power Reduction and Cross Talk Delay Minimization using Bus Encoding in Low Power VLSI” International conference on VLSI Design and Embedded systems,(ICVLSI’08),Velammal Engineering college,Vhennai,14-16 feb,2008.,pp.131-134.

**PAPERS PRESENTED IN SYMPOSIUM**

1. R.Kalyan, Dr. K.T.V.Reddy, Dr. K.Padma Priya” Compact Ultra-Wideband (UWB) Antenna With Band Notch Characteristics for Short distance wireless applications” IETE mid term symposium 2017.
2. K.Padma priya, CH.D.V.Paradesi Rao,“Modified Novel deep-submicron Coding for Low energy” National level symposium on Nano Electronics at Vagdevi Engineering collage, Warangal,1/12/2007.

**FDPs/WORKSHOPs ATTENDED**

1. Prof.k.padmapriya has participated in a one week on **national training programme on “IOT”** **for women scientists &technologists 29 October to 02 september, 2022, by ESCI,Hyderabad.**
2. Prof.k.padmapriya has participated in a one week on **line training on “ethics &values for scientists &technologists”September 20-24, 2021, by ESCI,Hyderabad.**
3. Prof.k.padmapriya has participated in a one week on **line training on “IOT”**

**October 25-29, 2021, by ESCI,Hyderabad.**

1. Prof.k.padmapriya has participated in a one week on **line PARTICIPATION IN SPS Seasonal School on Signal Processing and Communication Systems for 5G Participation on**

**September 13-17, 2021**

1. Prof.k.padmapriya has participated in a one week on line training on “INTERNET OF THINGS(IoT)”,Dec14-18,2020,conducted by ESCI,INDIA”,Hyderabad.
2. Prof.k.padmapriya has participated in a one week on line FDP on ”Recent Advances in Communications and Signal Processing” 24-29, Aug 2020 by JNTUVZM.
3. Prof.k.padmapriya, one day webinar “Impact of COVID -19 on the development of Rural economy- challenges and opportunities” on 27-07- 2020
4. Participated in a 5-Day-on line work shop on” online workshop on Universal Human Values on the theme “Inculcating Universal Human Values in Technical Education” during 25-29 April, 2020 as organized by All India Council for Technical Education(AICTE).
5. Participated in a seven day online short term training programme on “development of research proposals and funding opportunities”, Tirumala Engineering college, Narsaraopet, AP, June 3-9, 2020.
6. Participated in a 30 Days **Faculty Development Program On Data Science”.** 1-06-2020 to 30-06- 2020 by **Andhra pradesh state skill development corporation (APSSDC).**
7. Participated in a 5-Day-National Level Online Faculty Development Program on *ARTIFICIAL INTELLIGENCE* from *22nd May to 26th May 2020* organized by Department of CSE & IT in association with NYCI & Brain o vision Solutions India Pvt. Ltd.
8. One Week Online Faculty Development Program on “Research Challenges and Opportunities” Post COVID-19 (RECOP 2020)” 4-9 May 2020, Research & Development Cell and Institution’s Innovation Council SRI VASAVI ENGINEERING COLLEGE (AUTONOMOUS).
9. An AICTE five days on line work shop on “Incorporating Universal Human Values in Technical Education (An AICTE Initiative), 25-29, April, 2020,
10. A one week FDP sponsored by AICTE, Margadarshan Advanced Finite Element Methods and its Engineering Applications- Hands on Sessions“, JNTUKakinada, 9- 14, March, 2020.
11. A one week National Workshop on “Strategic Quality Initiatives in Technical education” **from 19-23 Dec 19  at Port Blair  by** **Engineering Staff College of India, Hyderabad.**
12. A one week AICTE Training Programme (ATAL) FDP on “Block Chain”, JNTUKakinada, 14-18, October, 2019.
13. A one day workshop on “Quality Enhancement, Assessment and Accreditation of Engineering Colleges”, organized by APSCHE & JNTUK on 11’November, 2019.
14. A one week FDP on “Advanced Vibration Analysis and its practical applications”, from 11’Feb 2019 to 16‘th Feb 2019, Mechanical Department, UCEK, JNTUK.
15. A one week FDP on “Recents Trends in Communication Engineering (LAB VIEW, FEKO, XILINX), from 4’Feb 2019 to 9‘th Feb 2019, ECED, UCEK, JNTUK.
16. A one week FDP on “Improving Teaching Skills in Network Analysis”, JNTUKakinada, from 29/10/18 to 3/11/18.
17. Participated in one week National Refresher course on “Improving Teaching Skills in Computer Programming”, JNTUKakinada, from 29/01/18 to 2/2/18.
18. Participated in one week FDP on “Improving Teaching Skills in Network Analysis” from 29/10/18 to 3/11/18.
19. Participated in one week FDP on “IoT Applications, conducted in ECE department, JNTUKakinada, from 22-27, Jan-18.
20. Participated in a two day National Level Workshop on “Learning Management Systems and Ed-Tech Tools (WLMSET-2017) conducted by Digital Monitoring Cell 22-23, December 2017 in JNTUKakinada.
21. Participated in a two week FDP on “Contemporary Advances in Materials & Manufacturing Engineering (CAMME-2017)” Funded by AICTE, conducted in JNTUKakinada, from 30-10-2017 to 10-11-2017.
22. Attended a one week FDP ”Recent Advancements in VLSI Technology and Design Using EDA Tools” 21-25, Feb,2017, JNTUKUCE, Kakinada
23. Participated a three day work shop ”Improving the Teaching Skills in Engineering Mechanics”13-15, Feb,2017, DFDC, JNTU Kakinada.
24. Attended a one day workshop on “Complementary Domains-VLSI& Embedded Systems (CDVES-13) on 30’th April 2013.
25. Participated one week work shop ”Recent Advancements in VLSI Technology and Design Using EDA Tools”20-24, July,2016, JNTUKUCE, Kakinada.
26. Participated one week national work shop”21’stCentury Teaching Methodologies” 24-28, october,2016, JNTUKUCE, Kakinada.
27. Attended a three days workshop on “Outcome Based Education and Accreditation”, 8-10, June, 2016 conducted by NBA Nodal Centre JNTUK, University College of Engineering, Kakinada.
28. Attended a two day workshop on “Advance Microcontrollers with Arduino (NW-AMC2015)” 23-24, July 2015, JNTUK College of Engineering, Kakinada.
29. Attended a one day workshop on “Awakening women” JNTUK University College of Engineering, Vizianagaram, 6’th March, 2014.
30. Attended a two day workshop on “VLSI & EDA Tools (VLSI-2014)” JNTUK University College of Engineering, Vizianagaram during 6’th-7’th, February 2014.
31. Attended a one day workshop on “Workshop for Training Resource Persons on Outcome Based Accreditation Phase-I”, 29-04-2013 conducted by NBA Nodal Centre JNTUK,University College of Engineering, Kakinada.
32. Attended a three days workshop on “Workshop for Training Resource Persons on Outcome Based Accreditation Phase-II”, 16’th-18’th May,2013 conducted by NBA Nodal Centre JNTUK,University College of Engineering, Kakinada.
33. Attended a three days “Faculty Development Programme” 13’th-15’th,June, 2013 in JNTUK, University College of Engineering, Vizianagaram.
34. Attended a two days national workshop on “Communication, Signal & Image Processing (COSIP-2013) during 28’th &29’th June, 2013 in JNTUK, University College of Engineering, Vizianagaram.
35. Attended a two days “Orientation Programme for Faculty” 16’th-17’th, June, 2013 in JNTUK,University College of Engineering, Vizianagaram.
36. Attended a one day workshop on “Workshop for Training Resource Persons on Outcome Based Accreditation Phase-I”, 13-09-2013 conducted by NBA Nodal Centre JNTUK,University College of Engineering, Vizianagaram.
37. Attended a three days workshop on “ Workshop for Training Resource Persons on Outcome Based Accreditation Phase-II”, 26’th-28’th Sep,2013 conducted by NBA Nodal Centre JNTUK, University College of Engineering, Vizianagaram.

**ORIENTATION COURSE ATTENDED**

1. Attended orientation course from 6/6/2007 to 4/7/2007 at Academic staff College, Osmania University, Hyderabad.

**MEMBERSHIP**

1. Appointed by JNTUKakinada as a university nominee for the governing body of the affiliated colleges, VITS college of Engineering , Sontyam and Mother Theresa Educational Society group of Institutions, Vissannapet for the academic years 2013-14 &2014-15.
2. A member to Women Redressal Cell, JNTUK University College of Engineering, Vizianagaram from 30/12/2013 to 25-10-2014.
3. IEEE member bearing No.93596602 from Aug 2015.

**CHAIRING AND GUEST LECTURES**

1. As a session chair for A NATIONAL LEVEL TECHNICAL SYNPOSIUM during 22-23, feb 2020 by ECED, UCEK,JNTUK,KAKINADA.
2. Acted as a resource person in a work shop in “Low Power VLSI Design” conducted by Mahaveer Institute of Science and Technology, Hyderabad,22nd March2007.
3. As a session chair in FESTIVITIES-2K11, A national level youth festival,11-12,March,2011, Vagdevi Institute of technology & Science, Proddutur.
4. As a session chair and key note lecture A national level students technical paper presentation contest on 10-09-2011. RSR Engineering college, Kavali.
5. As a session chair Technical Symposium Scintillace 2K-11, September 23-24, 2011. JNTUACE, Pulivendula.
6. As a session chair Technical Symposium EVINCE2K-12,14-3-2012 ,SKU college of Engineering, SKUniversity,Anantapur
7. As a session chair Technical Symposium,E-Merge-2012,Feb 23-24,2012 JNTU A Collecg of Engineering, Anantapur.
8. A guest lecture in “New advancements in the field of communication” on 6-11-2011. Rao Bahadur Y.Mahabaleswarappa Engineering College, Ballery.
9. A guest lecture in “New trends in Low power VLSI” on 24-10-2010. Rao Bahadur Y.Mahabaleswarappa Engineering college, Balleryguest lecture.
10. A guest lecture in “Low Power VLSI concepts”, 28-8-2011 NRI Institute of Technology,Visadala,Guntur.
11. A guest lecture in Low Power VLSI Techniques, on 4-3-2013,Narsaraopeta Engineering College, Narsaraopeta,Guntur district, AP..
12. As a session chair for “Student Technical Paper Presentation in Geneces”,GECFEST’14 , on 30-1-2014, Gudlavalleru Engineering College, Gudlavalleru.
13. A guest lecture in “Power Reduction in VLSI Design”, on 13-11-2014,,Dr.Samuel George Institute of Engineering and Technology, Markapur.
14. As a session chair for a National conference on “Recent Trends in Electronics and Communication Engineering (RTECE- 2014) on 14’th -15’th, March 2014, JNTUKUCE,Vizianagaram.
15. A guest lecture in “Low Power VLSI Techniques”, on 13-11-2014,A.M.Reddy Memorial College of Engineering and Technology, Narsaraopeta.

1. Acted as a resource person in a National Level Technical Symposium in “TECHNOSTRIED2K16”SSN Engineering College, Ongole, on 24-02-2016.
2. As a session chair for a National Level Technical Symposium in “ESPARKX2K16” conducted by ECE Department, JNTUK UCE, Kakinada during 27-28, Feb 2016.

17. As a resource person for one week work shop on ”Recent Advancements in VLSI Technology and Design Using EDA Tools”20-24, July,2 016, JNTUKUCE, Kakinada.

18. As a resource person for one week work shop on ”Improving Teaching skills in Electronic Devices &Circuits”27-31, March,2018, FDC,JNTUK, Kakinada.

19. A one week FDP on “Improving Teaching Skills in Network Analysis”, JNTUKakinada, from 29/10/18 to 2/11/18.

20. A Two weeks Summer Faculty Development Programme on “IMPORTANT ENGINEERING SUBJECTS ”Electronics and Communications engineering discipline from 25-05-2019 to 01-06-2019, JNTUK, Kakinada.

**WORKSHOPS/CONFERENCES ORGANISED**

1. Acted as a Chairman for one week FDP on “ IoT Applications”, conducted in ECE department, JNTUKakinada, from 22-27,Jan-18.
2. *Acted as a Chairman for one week FDP ”Recent Advancements in VLSI Technology and Design Using EDA Tools” 21-25, Feb,2017, JNTUKUCE, Kakinada.*
3. *Acted as a chairperson for one week work shop ”Recent Advancements in VLSI Technology and Design Using EDA Tools”20-24, July,2016, JNTUKUCE, Kakinada.*
4. *Acted as a chairperson for one week work shop ”Recent Advancements in VLSI Technology and Design Using EDA Tools” 20-24, July,2016, JNTUKUCE, Kakinada.*
5. Acted as a Co-ordinator for a Two day National Conference “VSPICE-2K15” during 6-7, November, 2015” at ECE Department, JNTUK UCE, Kakinada.
6. Acted as a co-ordinator for a one-day workshop on “soft skills” on 30/07/2014 at JNTUKUCE,Vizianagaram.

**(DR.K.PADMAPRIYA)**