



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA**  
**Kakinada – 533 003, Andhra Pradesh, India**  
**DEPARTMENT OF ELECTRONICS COMMUNICATION ENGINEERING**

IV Year-I Semester		L	T	P	C
		1	0	2	2
<b>Designer Tools</b>					

### **Syllabus**

Student can select designing tool related to topic of his interest (HFSS, Microwave Studio CST, Cadence Virtuoso, Synopsys, Mentor Graphics, Xilinx, ERTOS or any Equivalent Industrial Standard EDA Tool)

Student is supposed to practice the selected tool and implement an application that is having Industrial usage/Societal Sensitive application preferably from the research paper published in reputed journal (IEEE, Web of Science.)

Also, the student needs to prepare a detailed report on the implemented design with results to the departmental committee.