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Dr.Aruna Tripathy
Dept. of I&E,CET Bhubaneswar
Smt. K.Jhansi Rani
Dept. of ECE, UCEK, JNTUK, Kakinada

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Prof.Sribatsa Behera,CET Bhubaneswar
Dr. Chandrabhanu Mishra, CET Bhubaneswar
Dr. Tapas Kumar Patra,CET Bhubaneswar
and other Professors from IIT and NIT will review the papers.

Organizing Committee

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Dr. K. Rama Devi
Sri K.Durga Ganga Rao
Mr. K. Rajasekhar
Dr. R. Madhu
Smt.P.PushpaLatha
Smt. A. Rajani
Sri.Devi Prasad Dash
Ms.A.Bisoy

Contact Details

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Registration Details

Category	Fee (Rs.)
PG Students	1,500/-
Research Scholars/ Academicians	2,000/-
Industry Personnel	4,000/-

Authors of selected paper should send registration form along with DD in favor of "VSPICE 2K18" payable at UCEK, JNTUK, Kakinada on or before 5thJuly2018. The registration fees include local hospitality during the conference sessions and registration kit with conference proceedings. All other expenses are to be borne by the participants. Accommodation will be provided on prior request, on chargeable basis and subject to availability. For presenting the paper in the conference one of the authors (in case of multiple authors) has to register by paying the requisite registration fee by the dates given below.

Submission of Paper

Prospective authors are invited to prepare full text paper (not exceeding 4 pages) in IEEE format available on http://www.ieee.org/conferences_events/conferences/publishing/templates.html

Authors are requested to send the soft copy of the paper to the organizers by E-mail to nevspice2k18@gmail.com
The accepted and presented full papers only will be published in conference proceedings.

Important Dates

Last Date for Submission: 20-05-2018
(full text paper)
Acceptance Notification : 11-06-2018
Last Date for Online Registration : 23rdJune2018. All DD's should reach by 5thJuly 2018 with the registration form.
Camera Ready Copy: 05-07-2018
submission

A Two Day National Conference on VLSI Design, Signal Processing, Image Processing, Communications & Embedded Systems (VSPICE-2K18)

Sponsored by
TEQIP-III
(Technical Education Quality Improvement Programme-III)

In association with
CET Bhubaneswar



Technically Supported by IETE Kakinada Center



July 19-20, 2018

Co-ordinators
Dr.B.LeelaKumari
Dr.Aruna Tripathy
Smt.K.Jhansi Rani

Organized by
**DEPARTMENT OF ELECTRONICS AND
COMMUNICATION ENGINEERING**
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (A)
Jawaharlal Nehru Technological University Kakinada
Kakinada, A.P., India. www.jntuk.edu.in



IN ASSOCIATION WITH
**DEPARTMENT OF INSTRUMENTATION AND
ELECTRONICS ENGINEERING**
CET BHUBANESWAR

About the University

Jawaharlal Nehru Technological University Kakinada is formed after quadrifurcating JNTUH in the year 2008. University College of Engineering, Kakinada, is constituent college of this JNTU Kakinada, playing its role of imparting technological education and actively involving in research in the state of Andhra Pradesh since 1946. It is located in the coastal districts of Andhra Pradesh having 266 Engineering, Pharmacy and Management Colleges affiliated to this University. This institution has won the appreciation for judicious effective utilization of TEQIP - I and TEQIP-II funds and it is having a first rank in terms of impact evaluation factor and has been under TEQIP-III with academic milestones to be scaled.

About the Department

The Department of Electronics and Communication Engineering in this Institute is the first department established in the state of Andhra Pradesh as Telecommunication Engineering Department in 1958 and is one of the oldest in the country, and later renamed as Department of Electronics and Communication Engineering. The department has expertise in Communication Engineering, Signal and Image processing, Antennas and VLSI Design. The Department of Electronics and Communication Engineering, University College of Engineering, Kakinada, JNTU Kakinada has contributed many stalwart to serve the society through their committed academic legacy. Both the Department of Electronics and Communication Engineering and its alumni are mutually proud of their achievements. The department has celebrated Golden Jubilee Year in 2008.

The Department of Electronics and Communication Engineering has well qualified and experienced faculty with excellent infrastructural facilities. The Department offers both Under Graduate and Post Graduate programmes and also International Integrated Master Degree Programme (IIMDP) in association with Belkinge Institute of Technology, Sweden (BTH).

The department faculty guided and guiding number of research scholars in various fields of specializations. The Department is known for organizing academic interactions through conferences, seminars, symposiums and workshops.

About CET Bhubaneswar

The College of Engineering and Technology (CET) was established in the year of 1981 to meet the growing technical manpower need of the state. Initially it was under the control of Odisha University of Agriculture and Technology (OUAT). It became a constituent college of the Biju Patnaik University of Technology (BPUT), Rourkela with effect from 09.07.2002. At present the institute is

running in a new campus having an area of about 100 acres at Kalinga Nagar, Bhubaneswar about 2 kms away from Khandagiri. The college offers UG programs in 11 different disciplines besides offering PG programs in 18 different specializations. In the State of Odisha, it is one of the premier institutes for engineering education in terms of quality faculty, good students, excellent placement and other technical activities. The college is NAAC 'A' accredited and it got an academic autonomy status very recently in September 2017. The college has received funds under three TEQIP programs.

About the Conference

The conference aims at providing an opportunity for exchange of ideas and dissemination of knowledge among Academia, Industry, Research Scholars, Scientists, and Entrepreneurs for sustainable growth of the society. Contributions are invited from prospective authors from related areas. All contribution should be of high quality, Original and not published elsewhere or submitted for publication. During the review period, Papers will be reviewed by eminent Researchers in the respective areas. The proceedings copy of the conference will be published with an ISBN number.

The Organizers cordially invite Abstracts and Full Research Papers for presentation from all over the academic and industry community to participate/present and publish their research papers in the National Conference on 19th & 20th of July 2018, in association with CET Bhubaneswar and technically supported by IETE Kakinada center in the following scientific areas for presentations and discussions thereafter.

Call for Papers

VSPICE-2K18 invites original Submission in the following general or related areas:

- VLSI / Microelectronics
- Signal and Speech Processing
- Image and Video Processing
- Communications (Wireless, Mobile Radar & Microwave Applications/3.5G / 4G Mobile Networks)
- Embedded Systems Applications

Salient features of this conference:

- The conference shall be organized in association CET Bhubaneswar and technically sponsored by IETE Kakinada Center.
- Invited speakers from higher learning institutions, viz., IITs and NITs.
- The Proceedings of this conference shall be released with an ISBN number.
- Some of the selected papers will be published in a special issue of International Journal with ISSN number.
- The conference will also present a Best Paper Award.



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REGISTRATION FORM

July 19-20, 2018

Name:

Designation:

Qualification:

Organization:

Address for communication:

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Phone:

Fax:

E-mail:

Details of DD:

Is Accommodation Required: Yes/NO

Signature of the candidate

Date:

Signature & Seal

Director/Principal